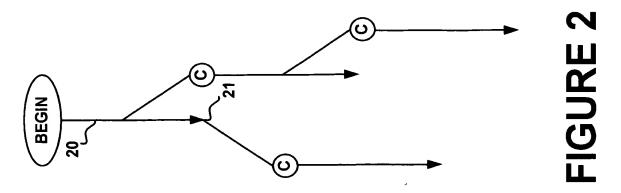
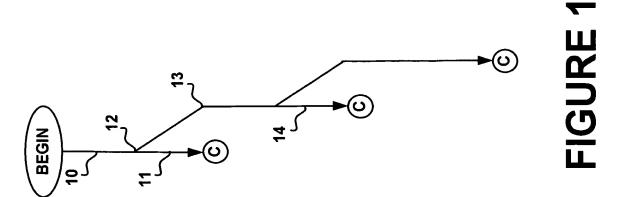
TITLE: A METHOD FOR TRANSLATING INSTRUCTIONS IN A SPECULATIVE MICROPROCESSOR FEATURING COMMITTING STATE

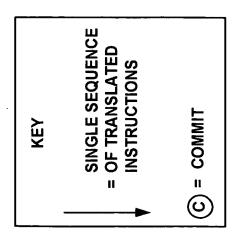
Inventor (s): Linus Torvalds, Robert Bedicheck, and Stephen Johnson
USSN: 09/417,980 Attorney Docket #: TRAN-P012

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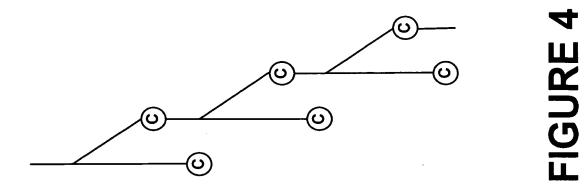


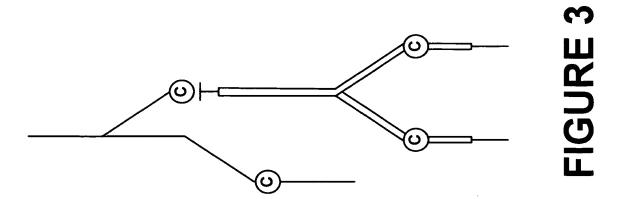
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Inventor (s): Linus Torvalds, Robert Bedicheck, and Stephen Johnson

USSN: 09/417,980 Attorney Docket #: TRAN-P012

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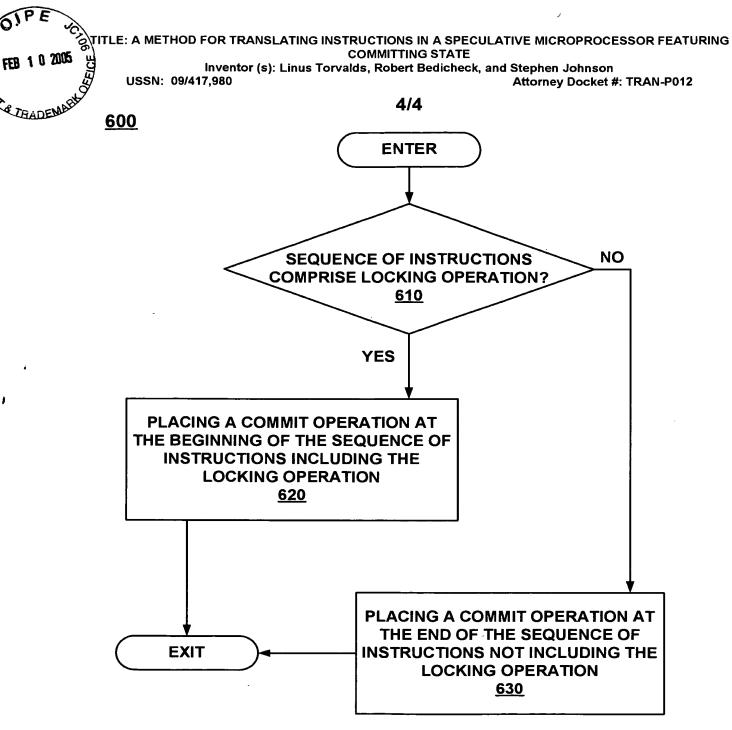


FIGURE 6